

FIGURE 1. Non-temporal Layout Parameters

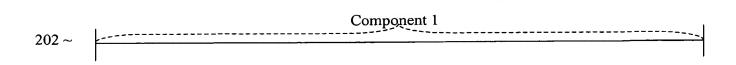


FIGURE 2a.

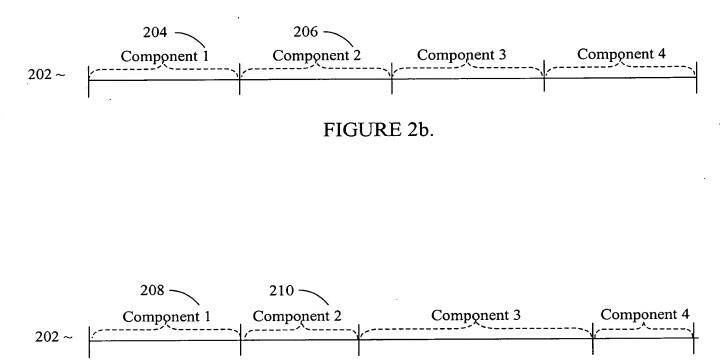
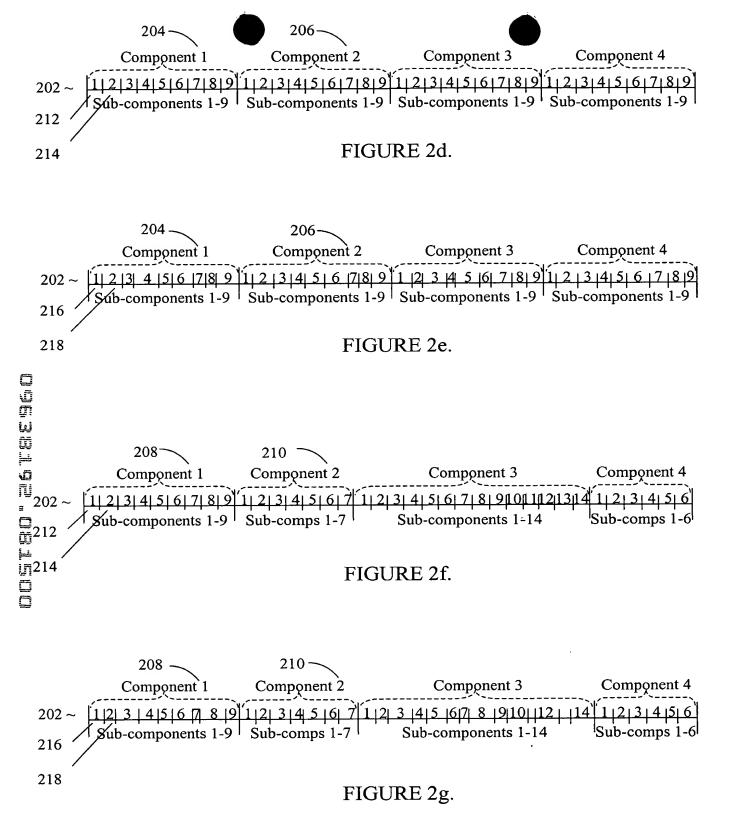
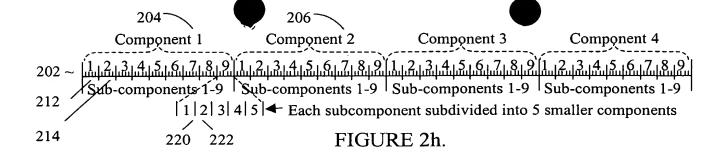
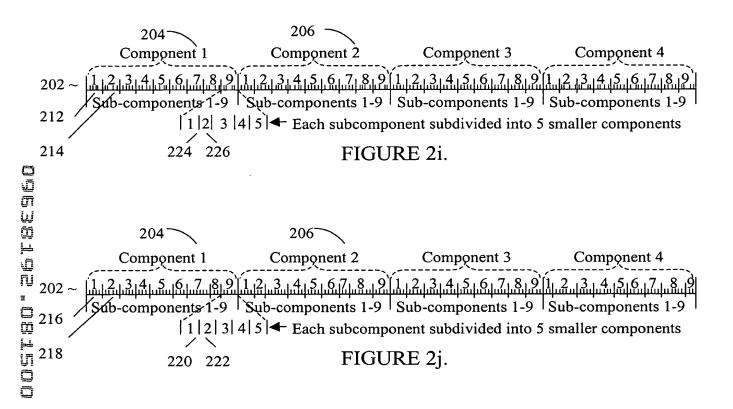
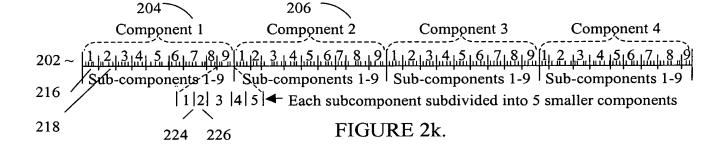


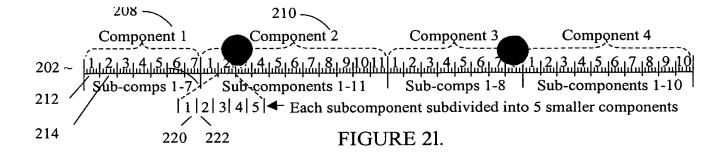
FIGURE 2c.

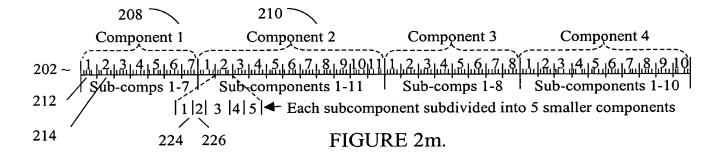


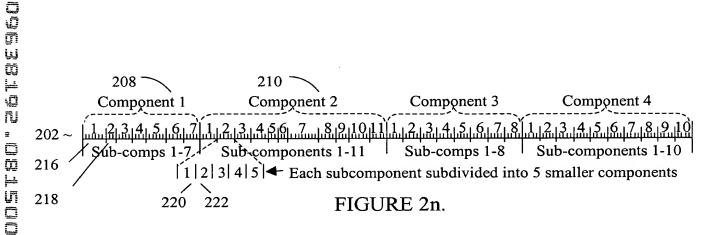


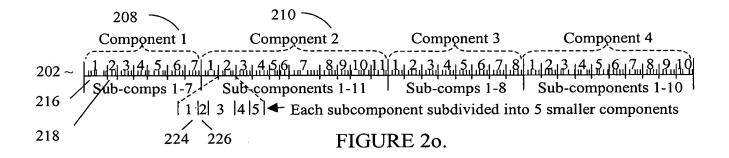












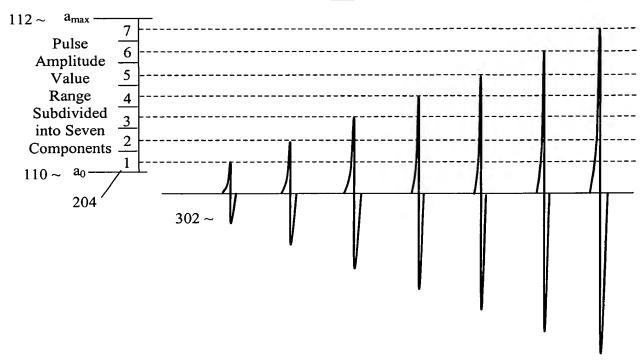


FIGURE 3a

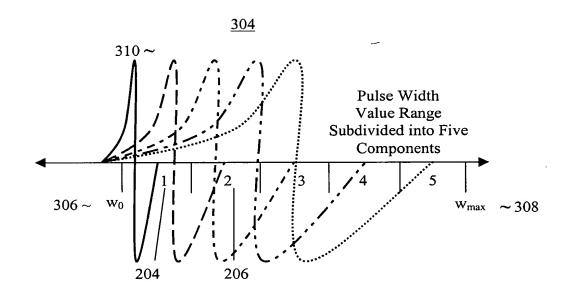
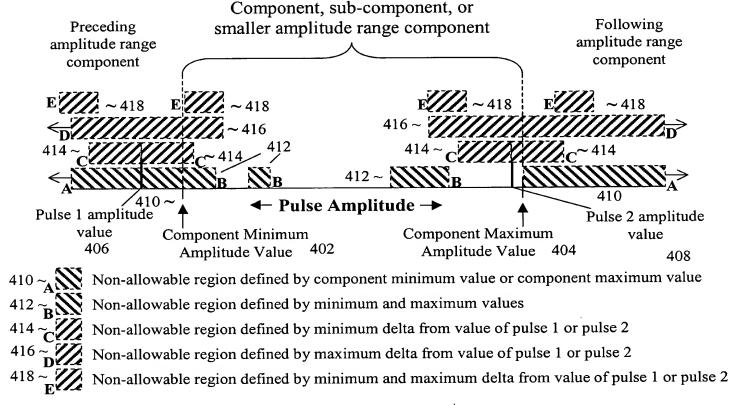


FIGURE 3b

Non-Allowable Regions - Viewed Separately



Non-Allowable Regions - Combined

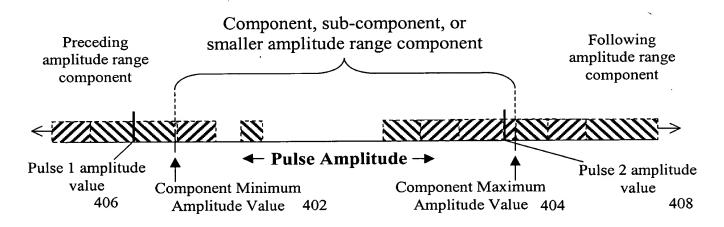
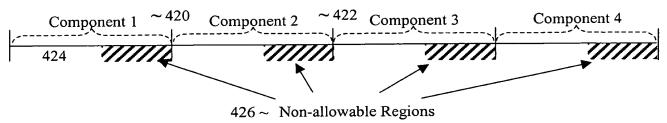
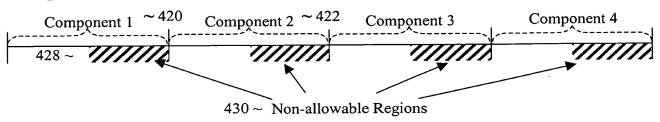


FIGURE 4a. Non-allowable Regions

Example 1: Non-anowable Regions of Smaller Size Than Allowable Regions



Example 2: Non-allowable Regions of Same Size as Allowable Regions



Example 3: Non-allowable Regions of Larger Size Than Allowable Regions

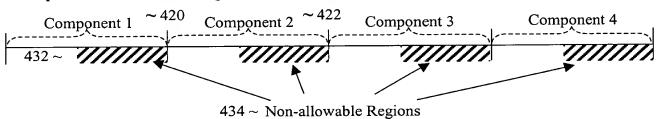


FIGURE 4b.

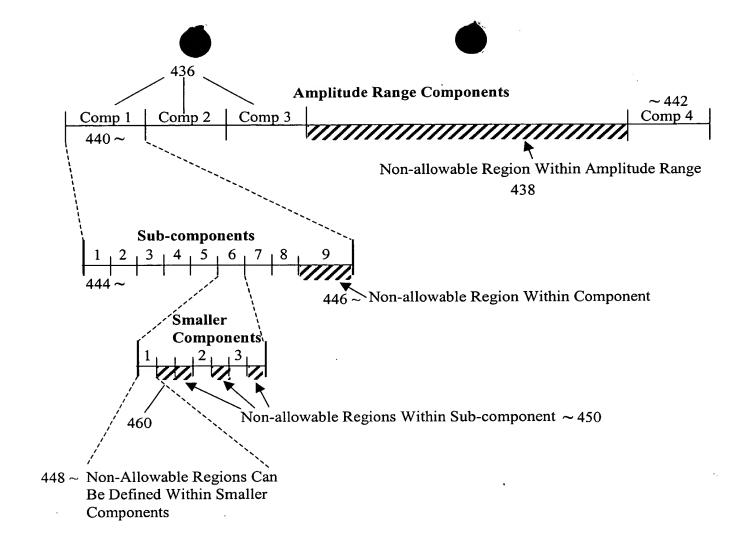


FIGURE 4c.

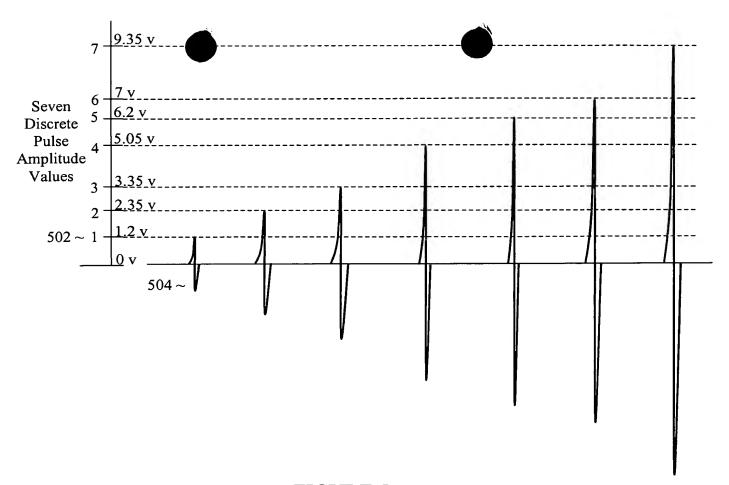


FIGURE 5a

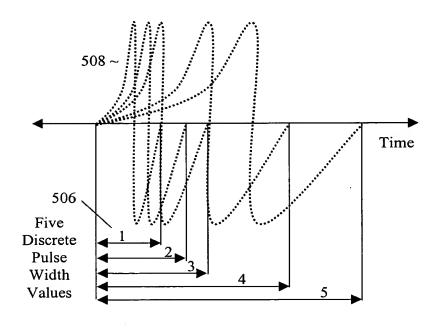


FIGURE 5b

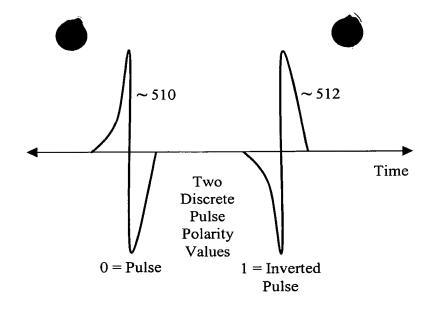


FIGURE 5c

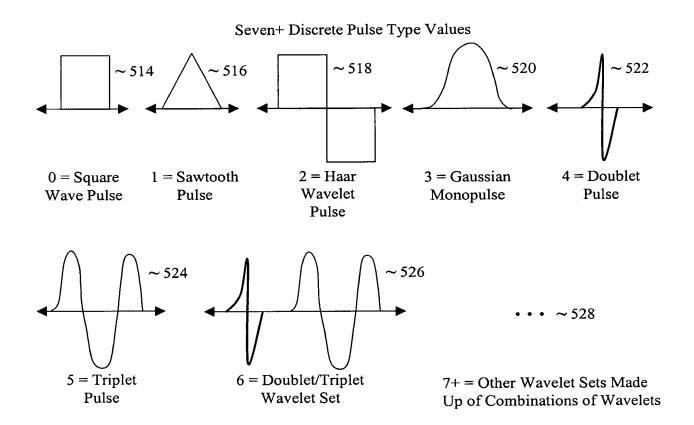
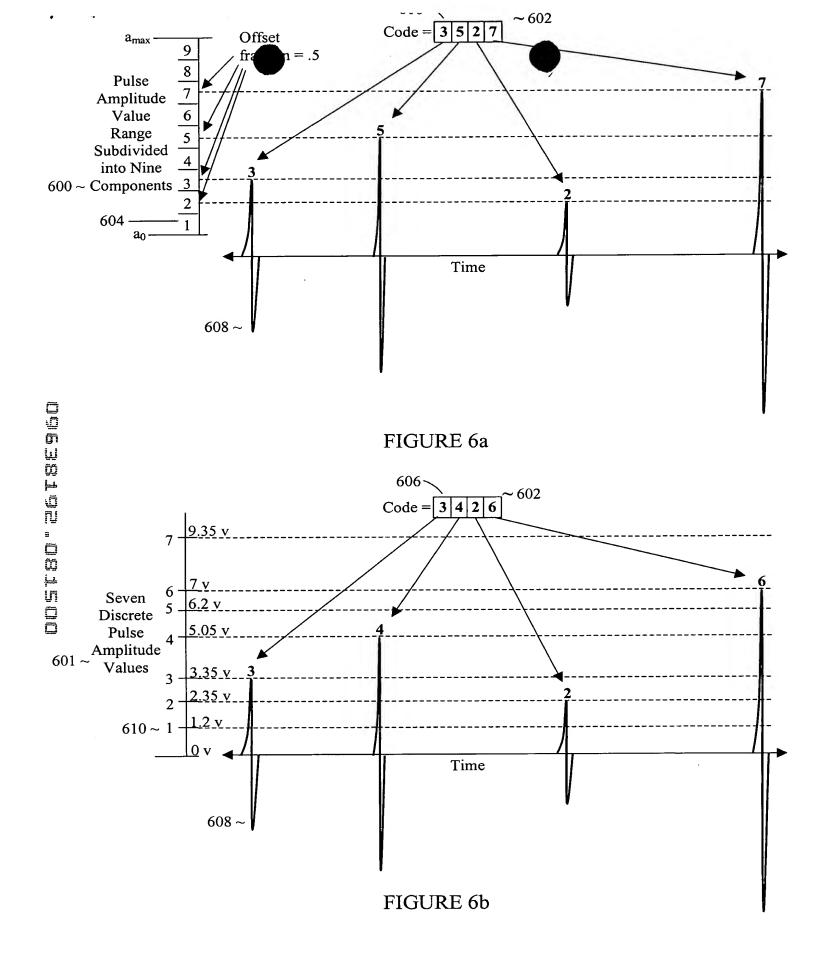


FIGURE 5d



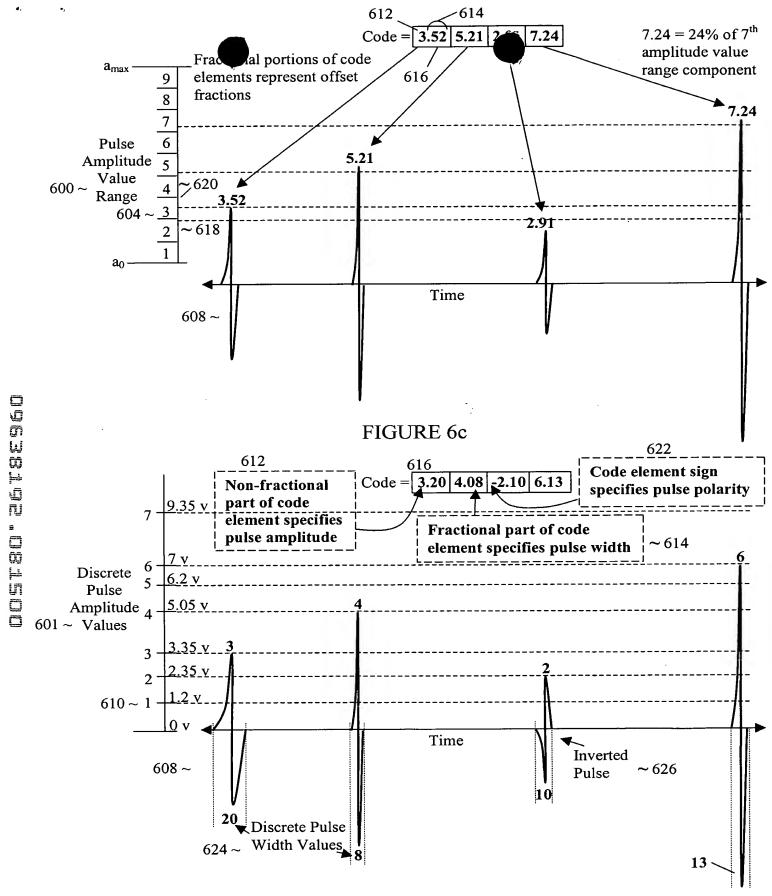


FIGURE 6d

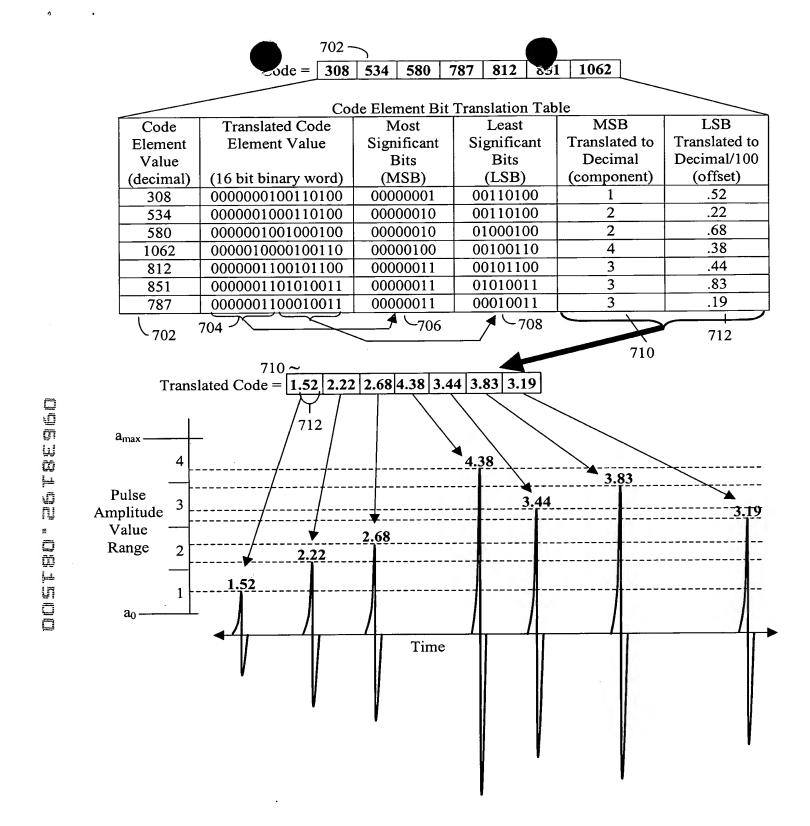


FIGURE 7a

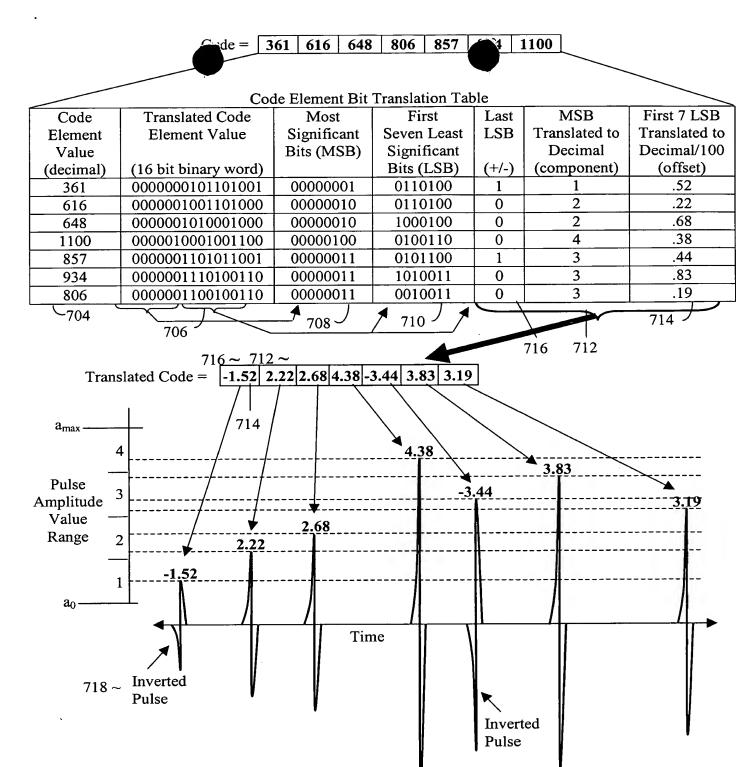


FIGURE 7b

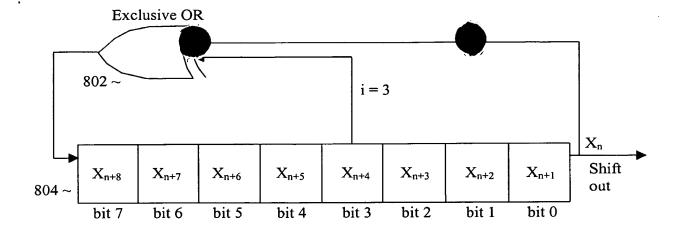


FIGURE 8a. Linear Feedback Shift Register

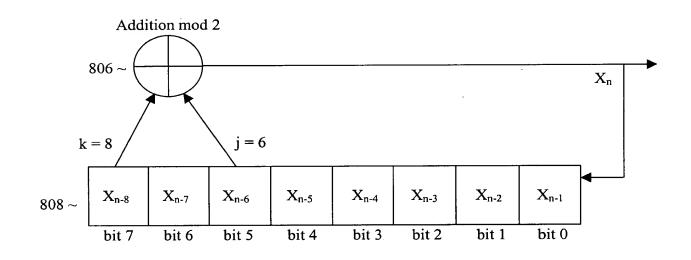
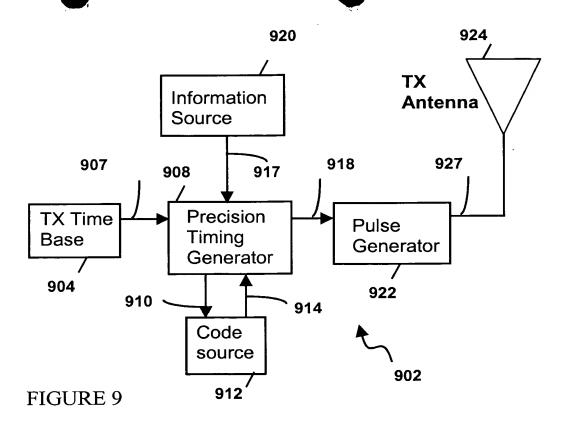


FIGURE 8b. Lagged-Fibonacci Shift Register Generator



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